**Testing voltages on outputs AD8302 board interferometer Jon Lustrup vs (Andrew Thornett) 26/1/2024**

* **Jan Lustrup results without brackets**
* **Andrew Thornett results in brackets**

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| **1420MHz +VM measurment** |  | **RF power off**  ***+VM pin (VMAX)*** | **RF @ -30dBm**  ***+VM pin*** |
| generator to port A (port B dummyload) |  | 0.932V (0.954V) | 1.630V (1.892V) |
| generator to port A (port B left open) |  | 0.920V (0.946V) | 1.615V (1.904V) |
| generator to port B (port A dummyload) |  | 0.933V (0.955V) | 0.234V (0.079V) |
| generator to port B (port A left open) |  | 0.942V (0.948V) | 0.245V (0.072V) |
|  |  |  |  |
| **1420MHz +PM measurment** |  | **RF power off**  ***+PM pin (PHASE)*** | **RF @ -30dBm**  ***+PM pin*** |
| generator to port A (port B dummyload) |  | 1.016V (0.985V) | 1.206V (1.308V) |
| generator to port A (port B left open) |  | 1.016V (0.980V) | 1.250V (1.254V) |
| generator to port B (port A dummyload) |  | 1.013V (0.992V) | 1.200V (1.282V) |
| generator to port B (port A left open) |  | 1.010V (0.989V) | 1.226V (1.216V) |
| **Generator to Port A dummy load on Port B, various power levels on generator and voltage outputs:** | | | |
|  | ***Not sure whether Jan used VP or VM pin here*** | | ***VM/VP Pins*** |
| -30dBm | 1.625V | | (1.894V)/(1.302V) |
| -33dBm | 1.574V | | (1.805V)/(1.225V) |
| -36dBm | 1.519V | | (1.731V)/(1.157V) |
| -39dBm | 1.466V | | (1.657V)/(1.108V) |
| -42dBm | 1.408V | | (1.575V)/(1.070V) |
| -45dBm | 1.373V | | (1.500V)/(1.045V) |
| -47dBm | 1.267V | | (1.448V)/(1.029V) |
| -50dBm |  | | (1.369V)/(1.013V) |
| -60dBm |  | | (1.138V)/(0.984V) |